

Notice of References Cited	Application/Control No. 10/509,370	Applicant(s)/Patent Under Reexamination SUGAWARA ET AL.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2002/0014666	02-2002	Ohmi et al.	257/368
	B	US-5,618,382	04-1997	Mintz et al.	216/64
	C	US-6,357,385	03-2002	Ohmi et al.	118/723AN
	D	US-5,685,949	11-1997	Yashima, Koji	438/694
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 161, 514.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.